

AN: PAT 1992-277474

TI: Redundant data processing system e.g. for control applications uses program counter and count comparator for synchronising stand-by system following interrupt

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AB: The redundant data processing system contains at least two data processing devices contg., central units (ZE1, ZE2) and arrangement for identifying interrupt events and for synchronisation following an interrupt. The central unit, which has been carrying out a program until an interrupt signal occurred, is held in a wait state until the other central unit has reached the program state of the held central unit. Each central unit contains a program counter (PZ1, PZ2) and a program count comparator (VG1, VG2) controlling the hold mechanism.; For high reliability data processing or control systems. The redundant data processing system synchronises the standby system very rapidly following an interrupt.

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FIG 1

